1 1

FIG.1

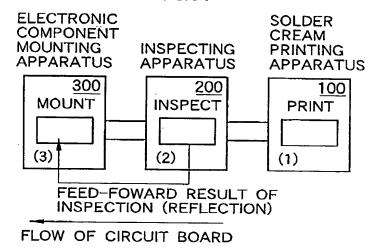
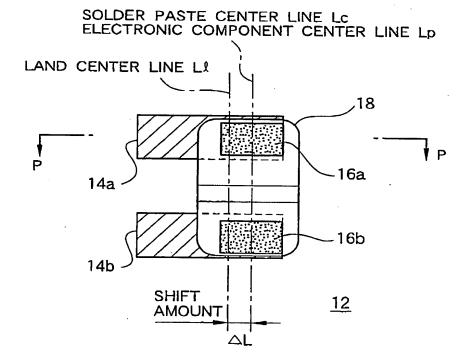
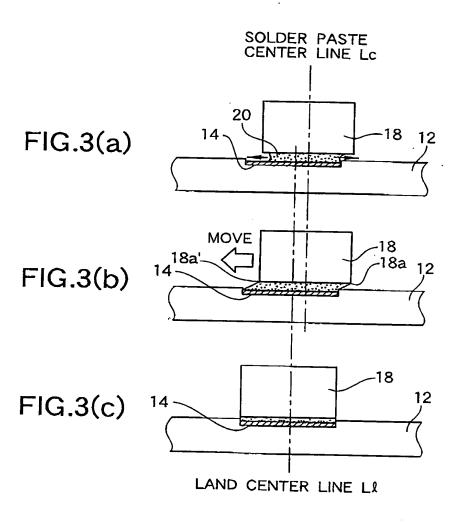
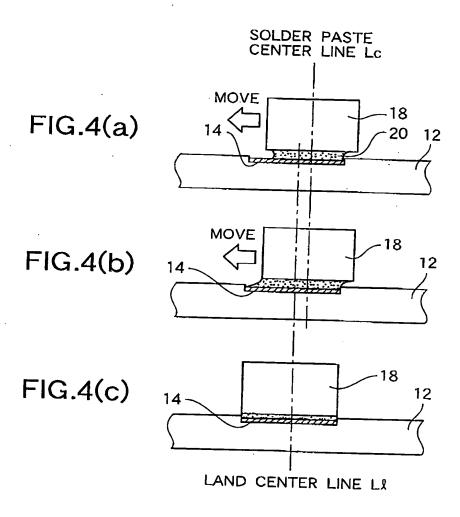


FIG.2







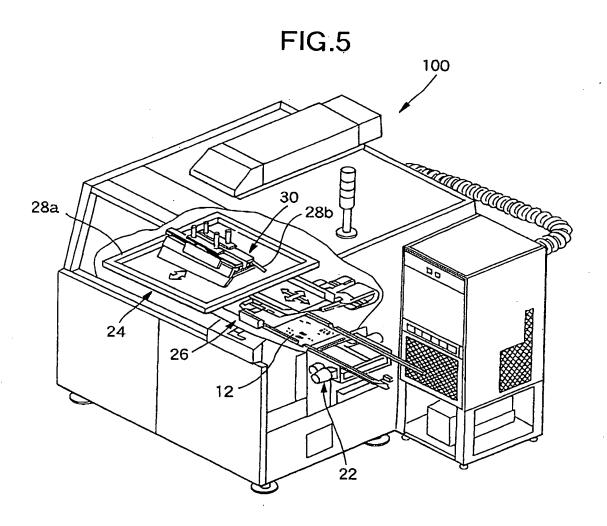
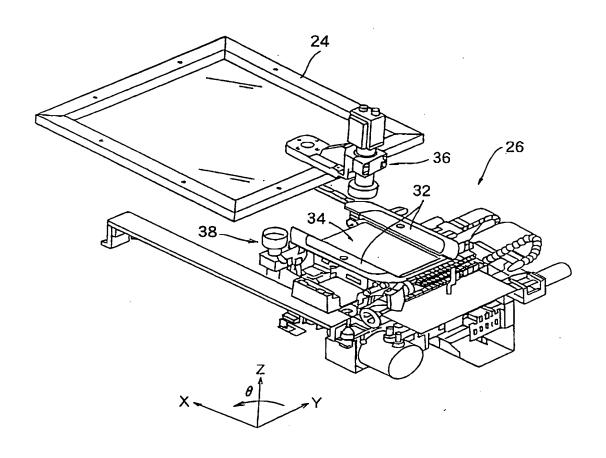
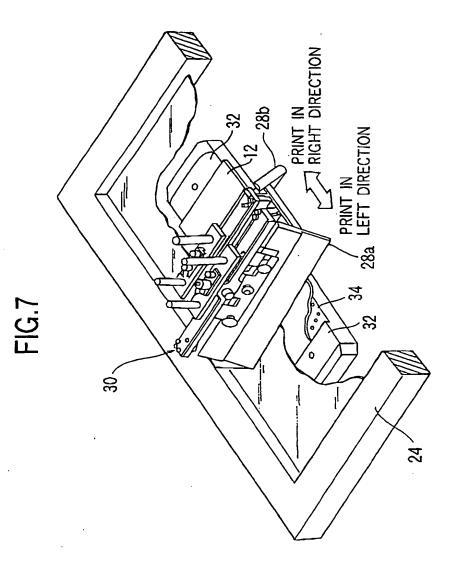
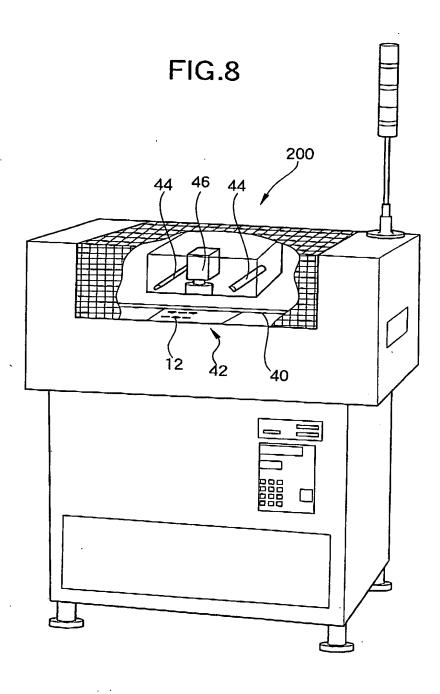
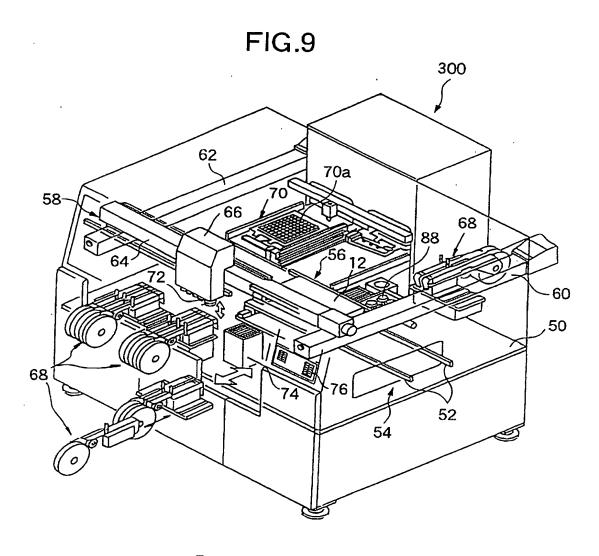


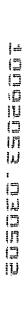
FIG.6











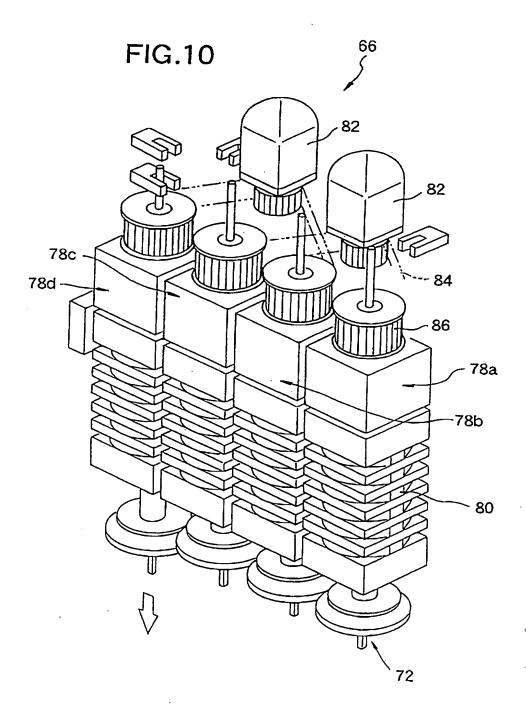


FIG.11

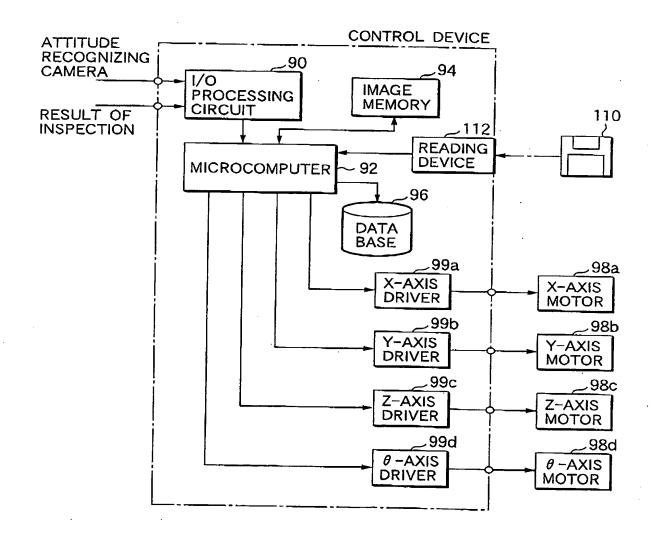
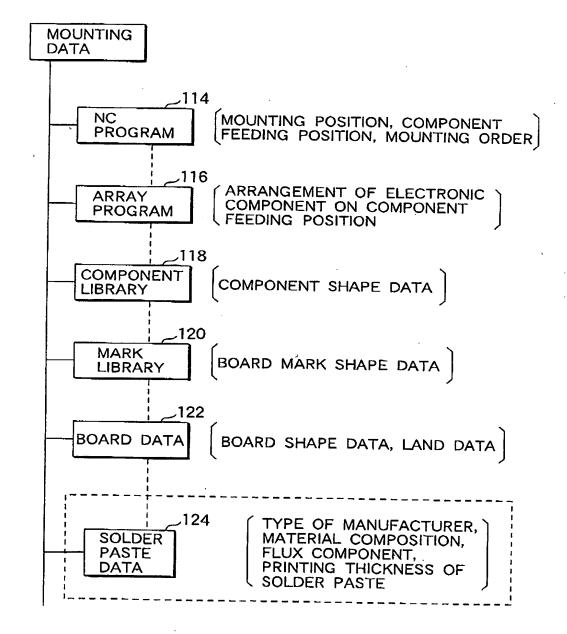


FIG.12





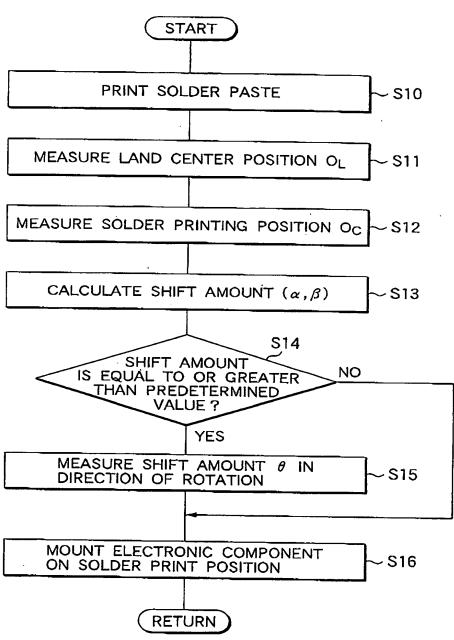
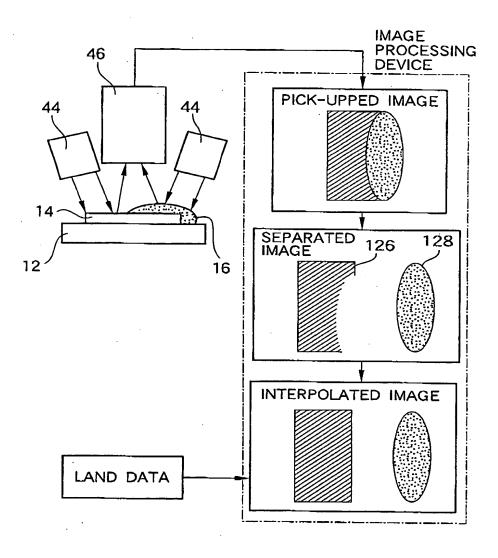
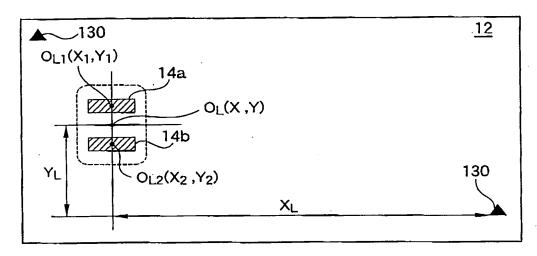


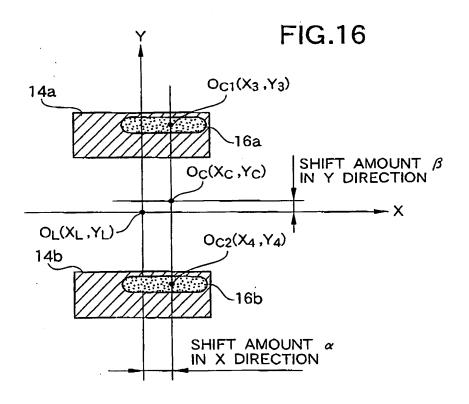
FIG.14

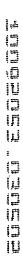


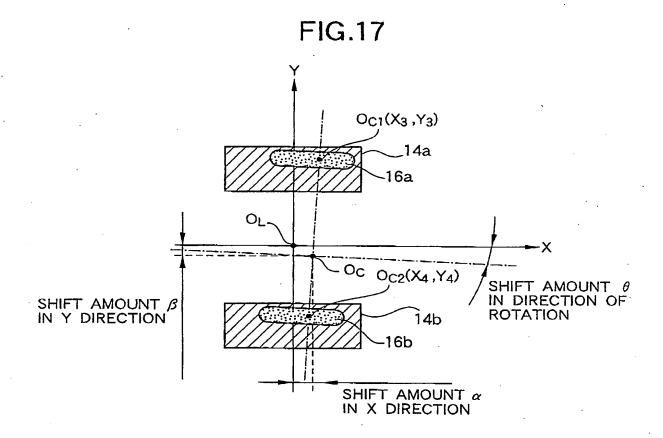
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FIG.15









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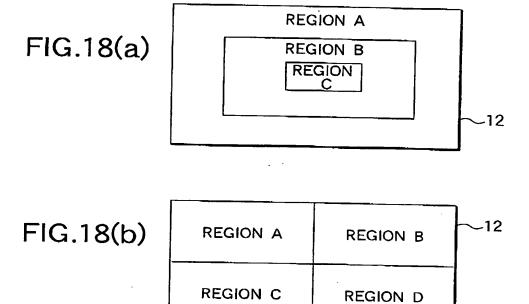
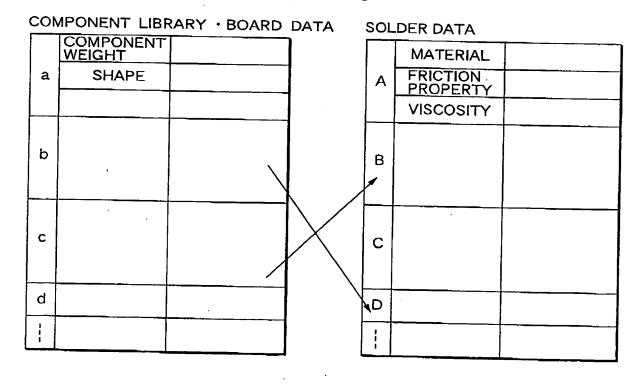
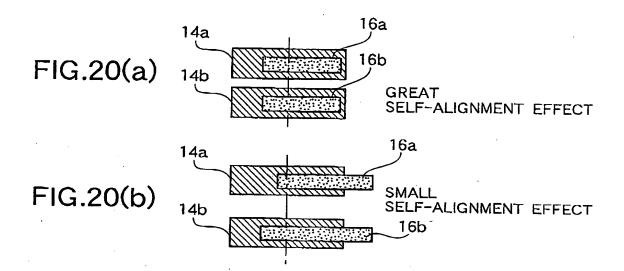
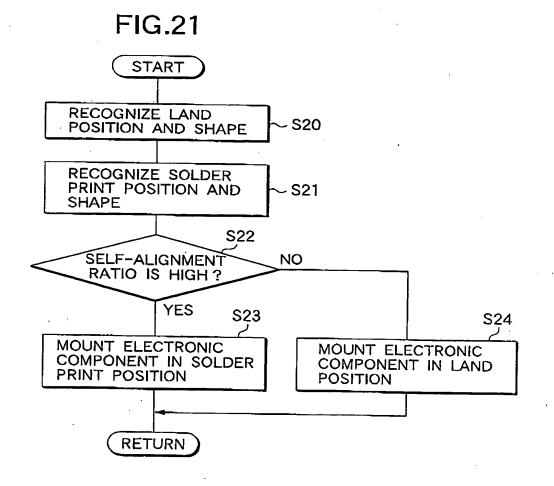
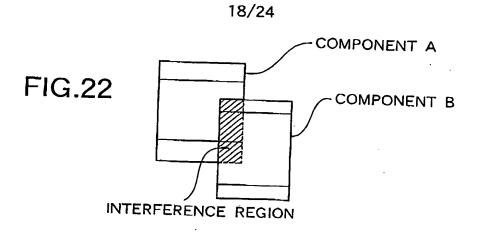


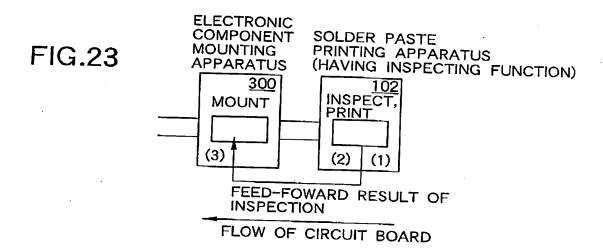
FIG.19











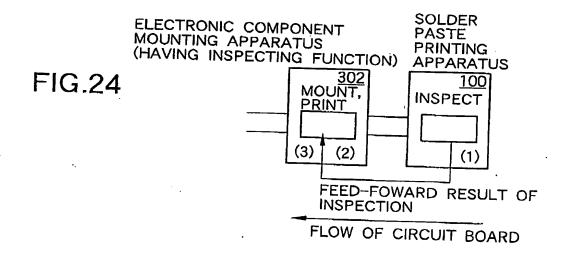
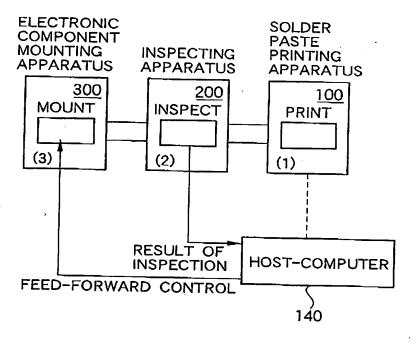


FIG.25



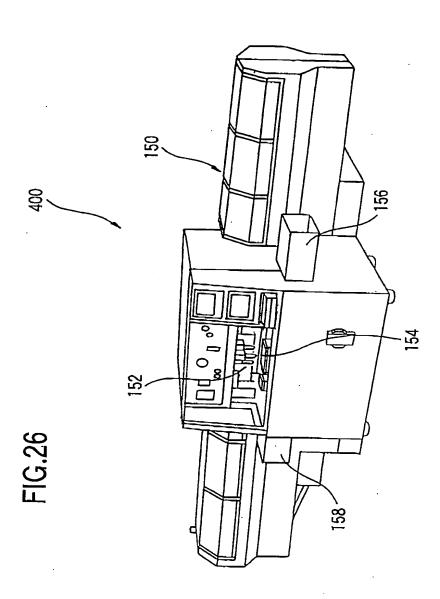
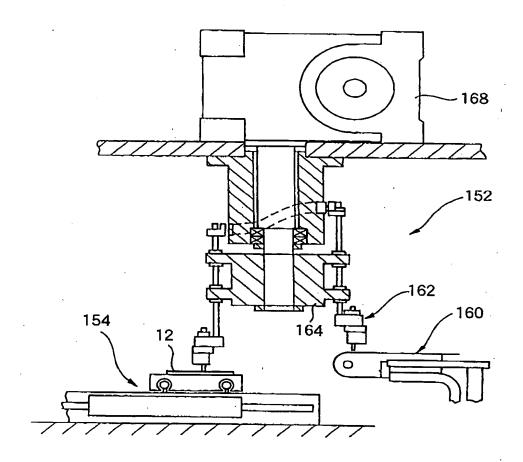


FIG.27



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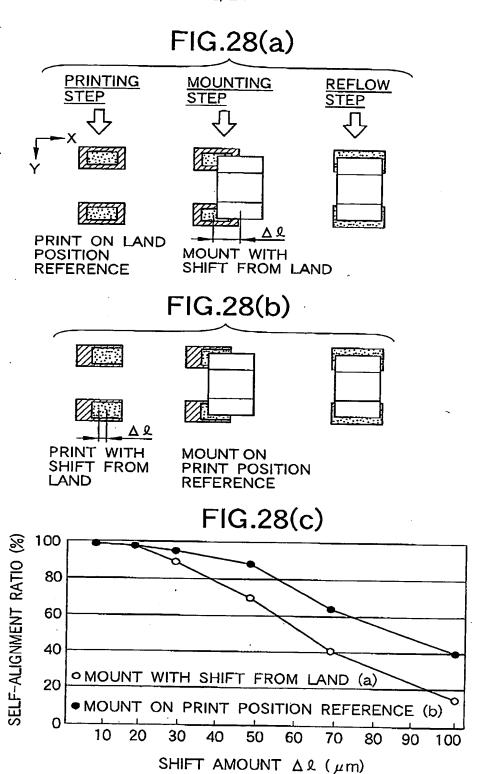
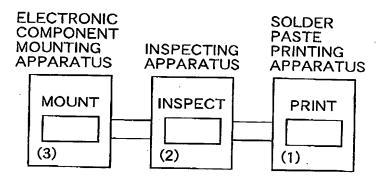


FIG.29



FLOW OF CIRCUIT BOARD

FIG.30(a)

FIG.30(b)

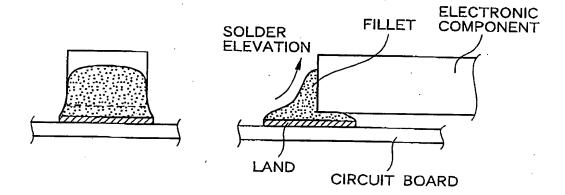


FIG.31(a)

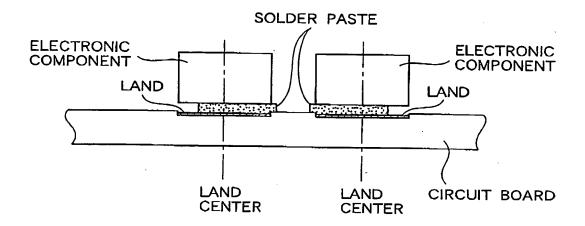


FIG.31(b)

